## In the Specification:

[0001] The disclosure extends upon and incorporates herein by reference patent application Ser. No. 09/803,588 09/803,599, filed Mar. 9, 2001 "Adapting Scan Architectures for Low Power Operation", and patent application Ser. No. 09/803,608, filed Mar. 9, 2001 "Adapting Scan-BIST Architectures for Low Power Operation".